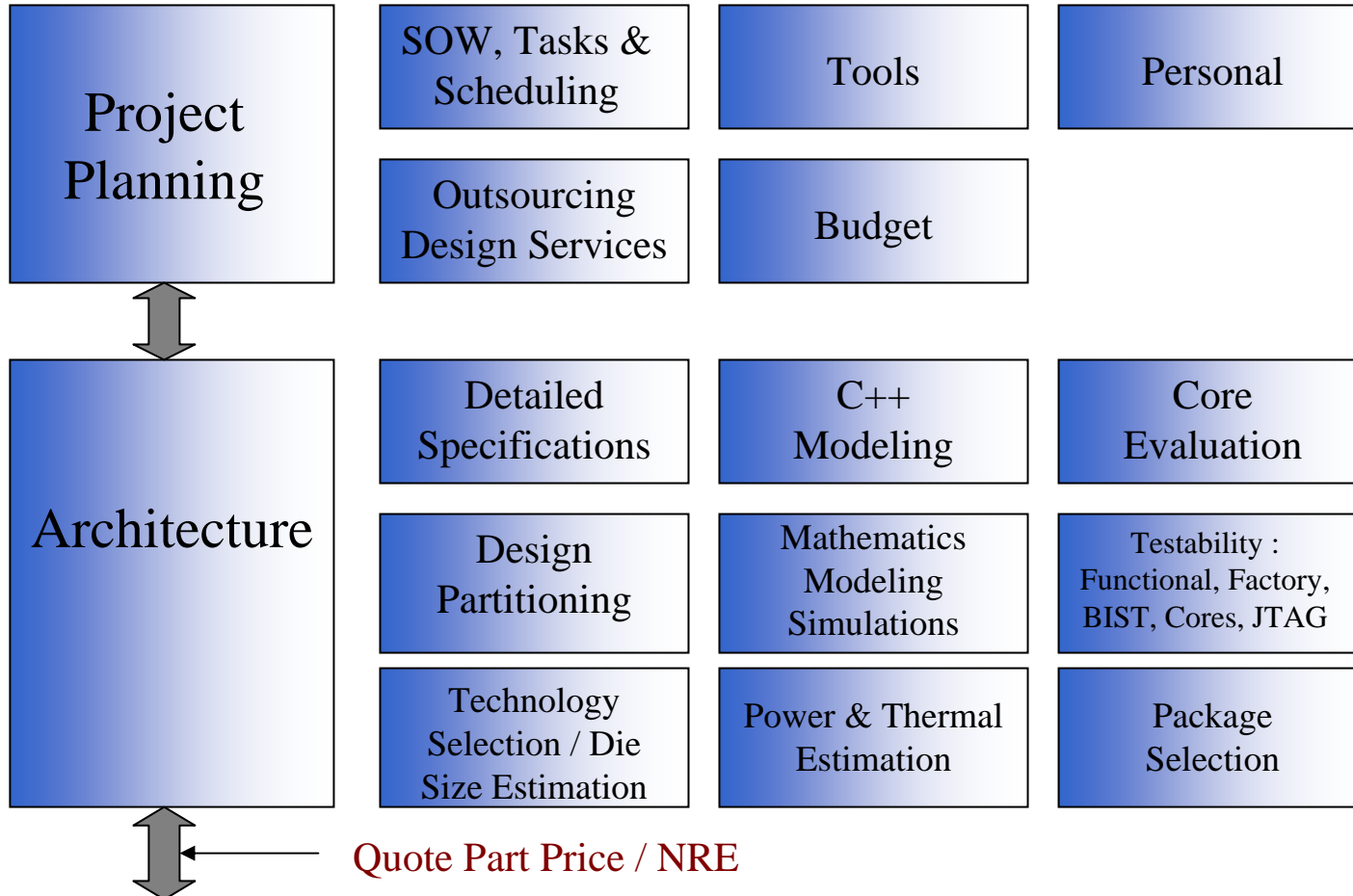
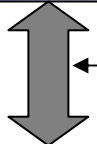
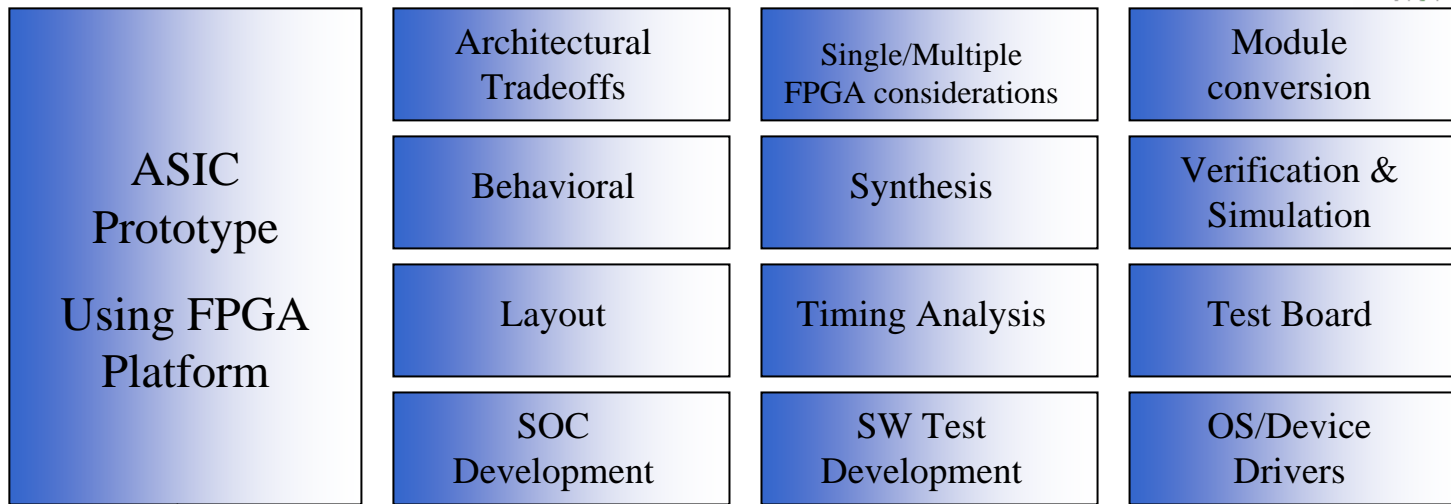


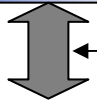
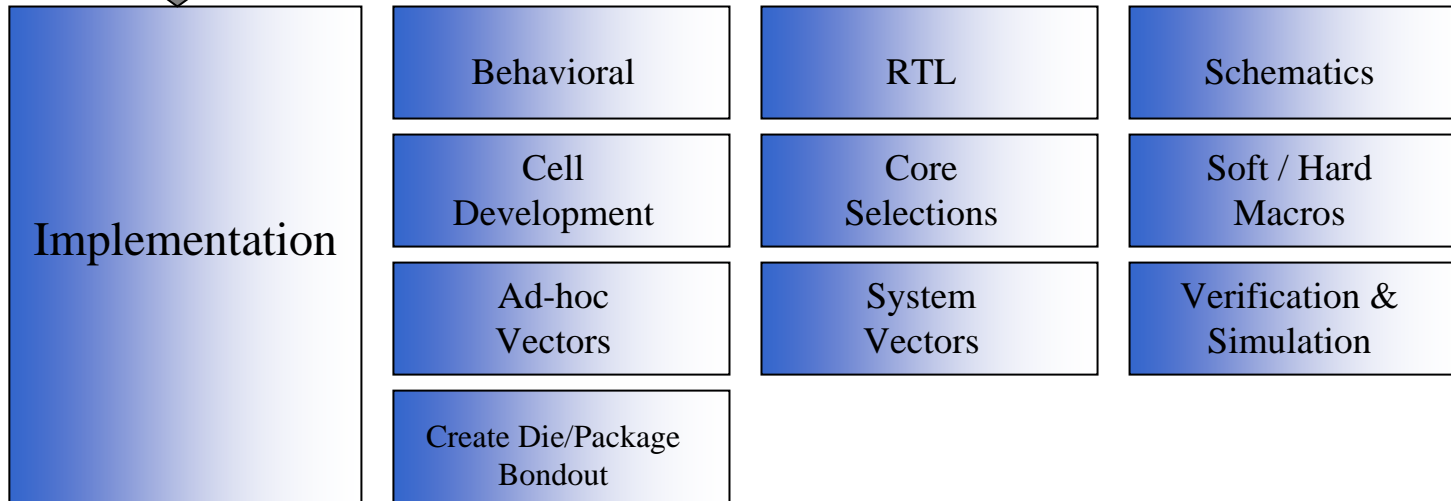
Flow & Tasks : ASIC / SOC; Structured ASIC



Vendor Selection - For Full Turn-Key ASIC Project
 (Base on Specification Platform)



Vendor Selection – RTL SignOff (Base on FPGA Platform)



Vendor Selection – RTL SignOff (Base on Simulation Platform)

